

FIG. 2

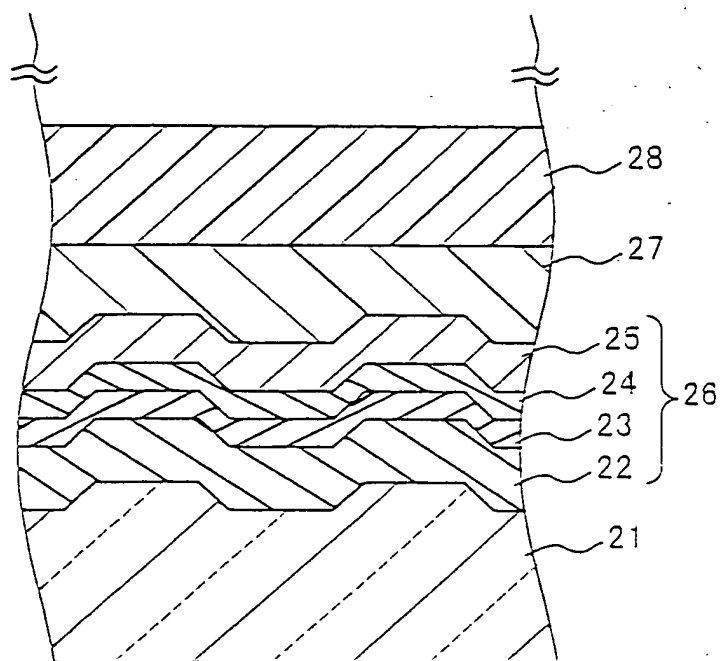
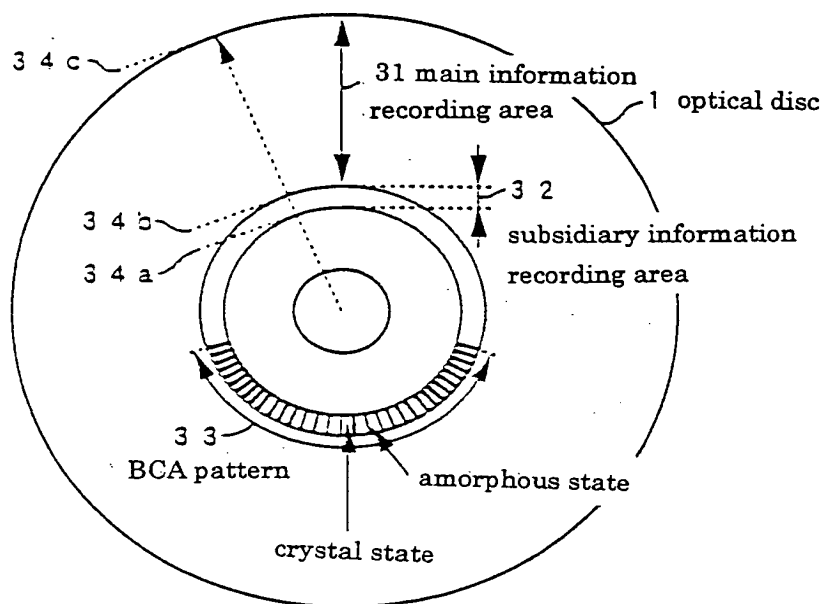


FIG. 3



09/786877-031201

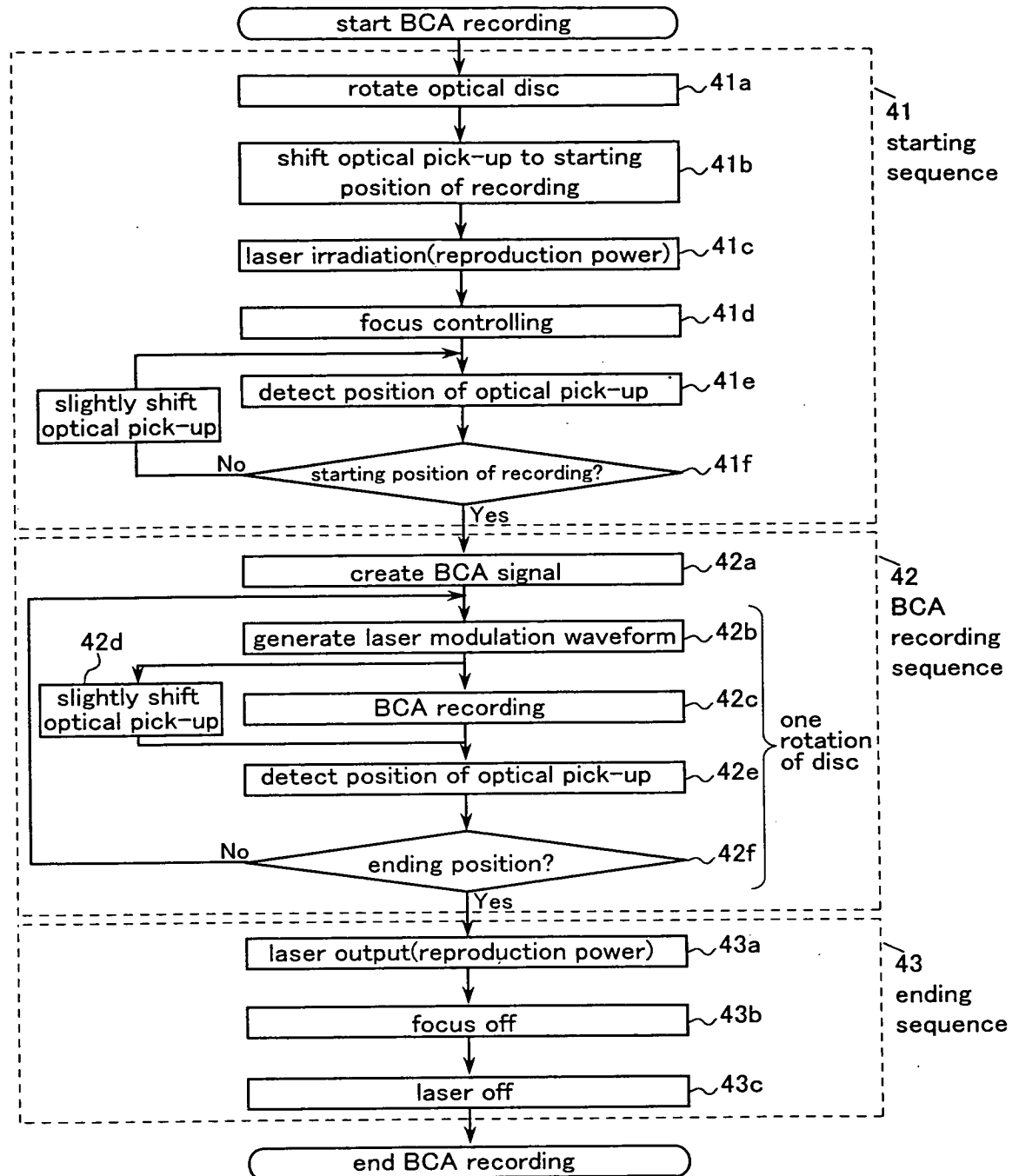


FIG. 4

FIG. 5

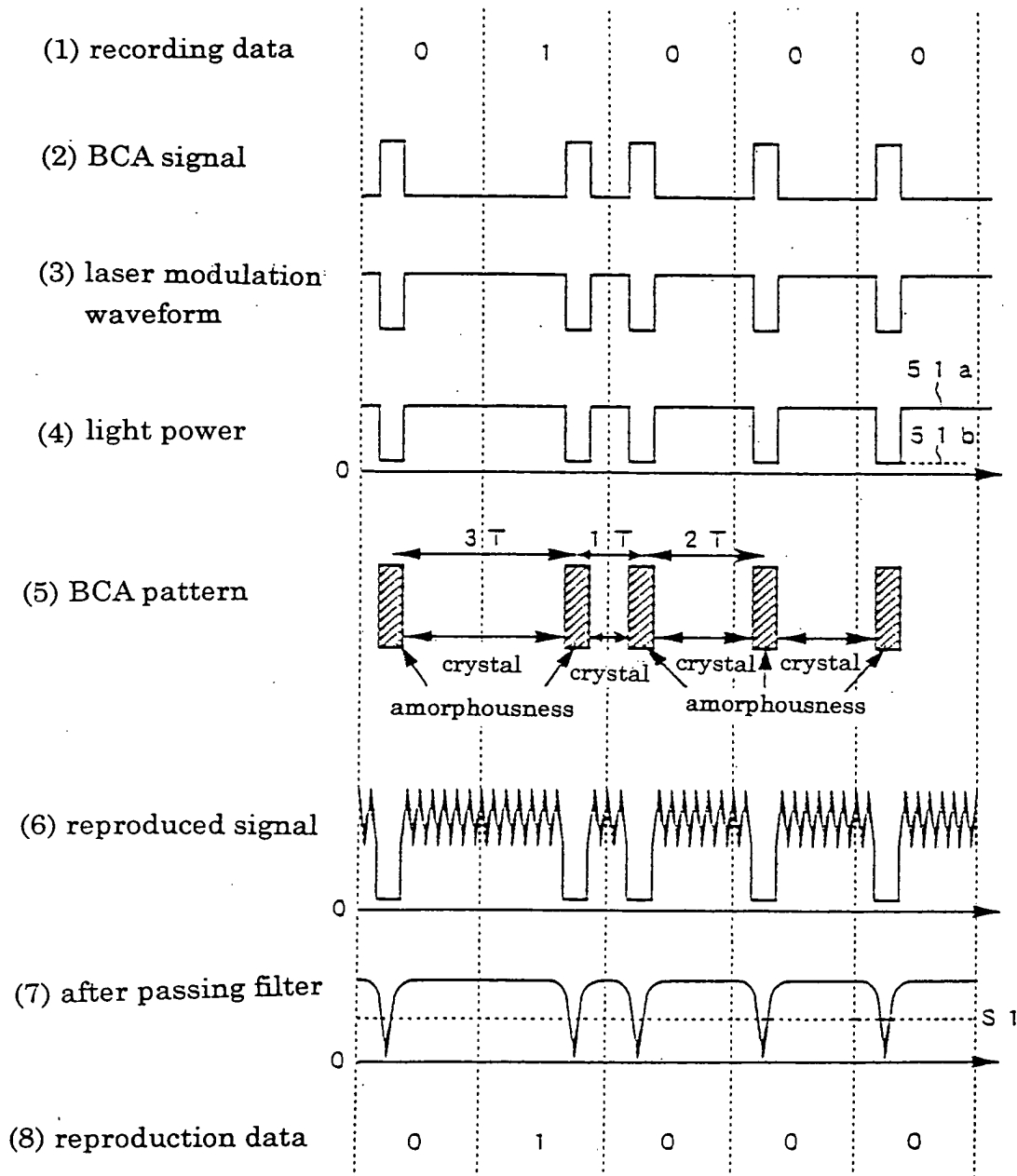
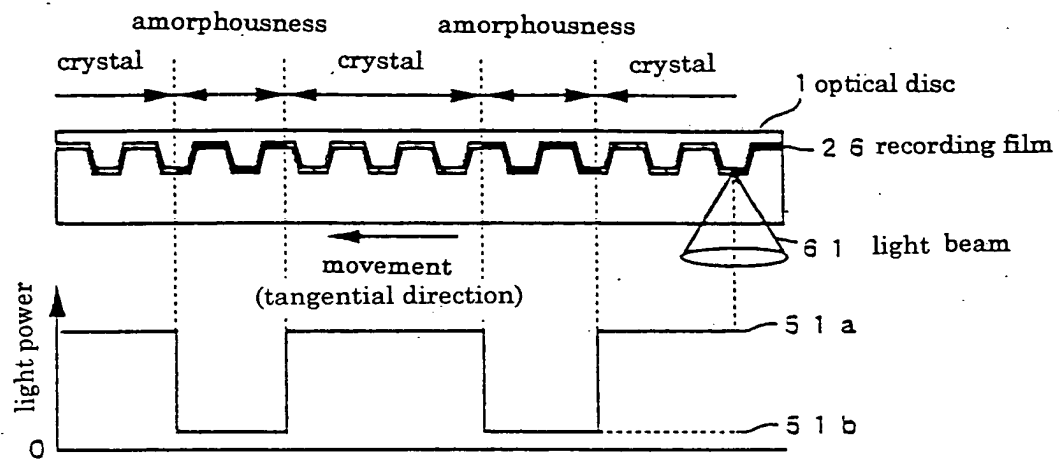
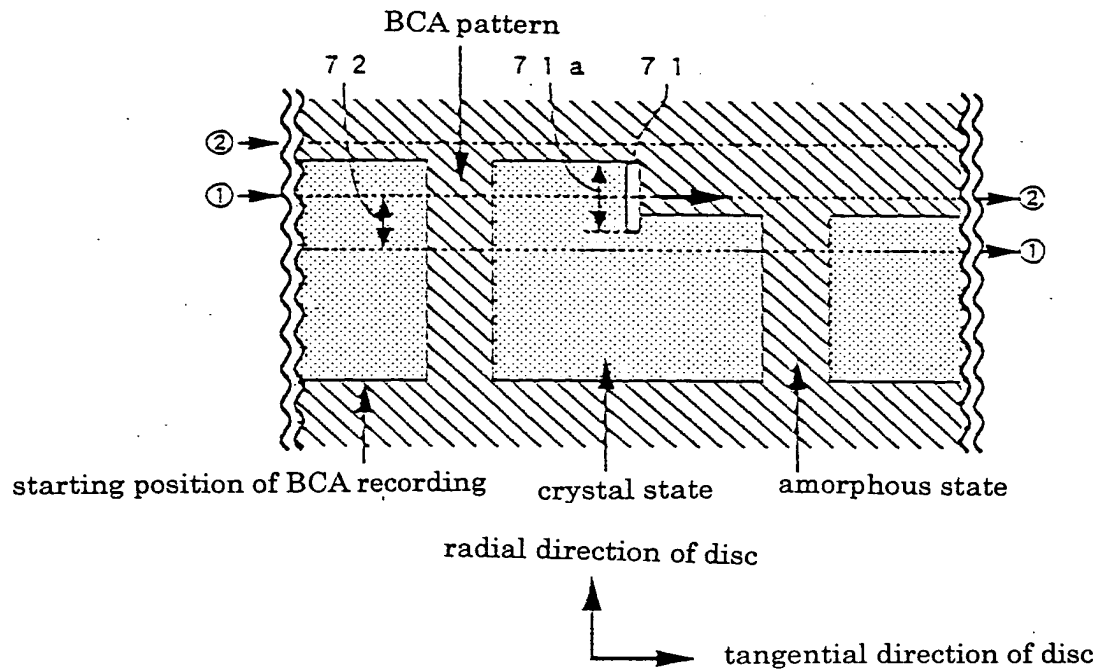


FIG. 6



09/786877-031201

FIG. 7

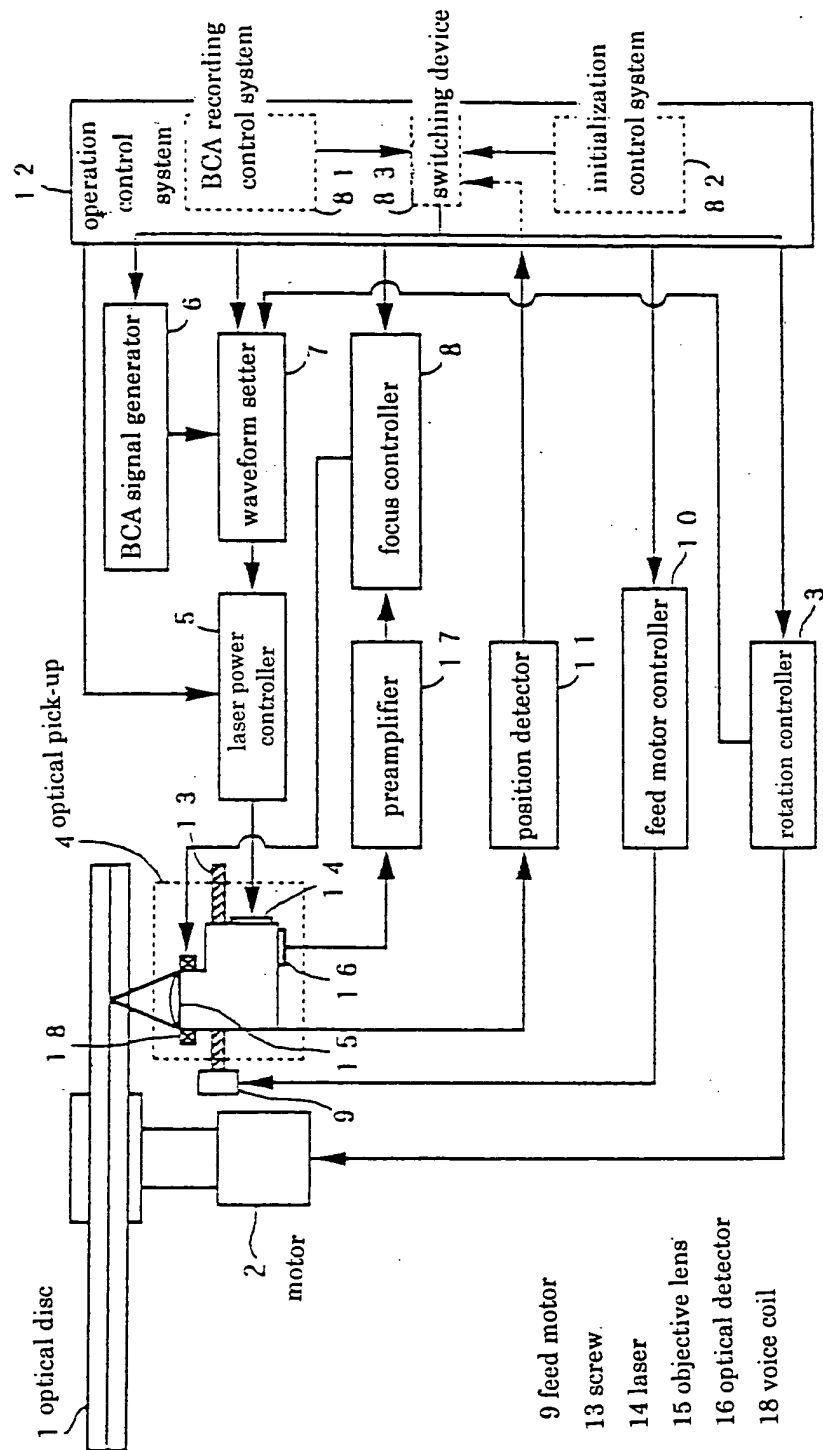


71 focus spot

71a length of focus spot in radial direction

72 moving amount of optical pick-up (per one cycle of disc)

FIG. 8



9 feed motor

13 screw

14 laser

15 objective lens

16 optical detector

18 voice coil



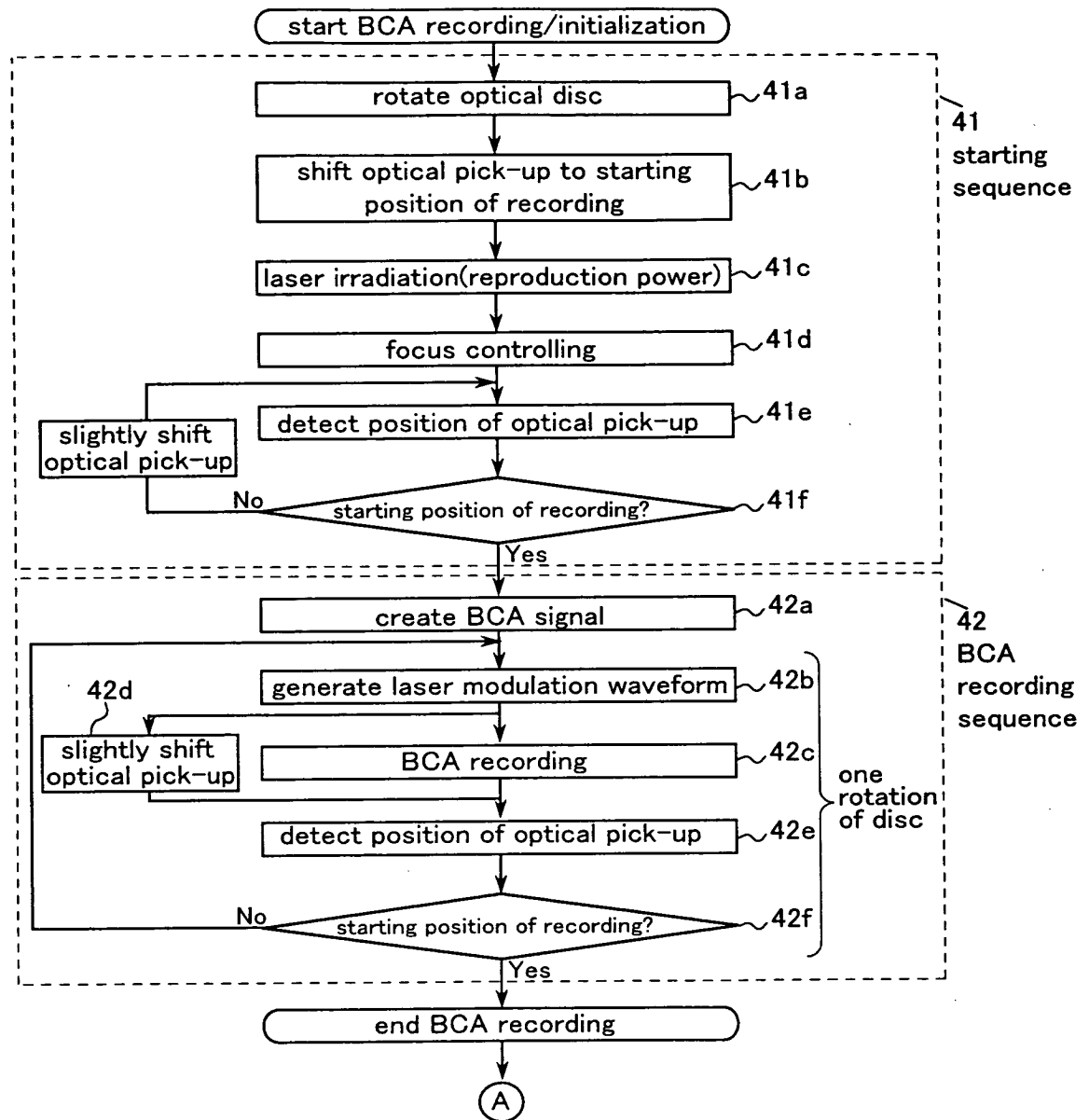


FIG. 9

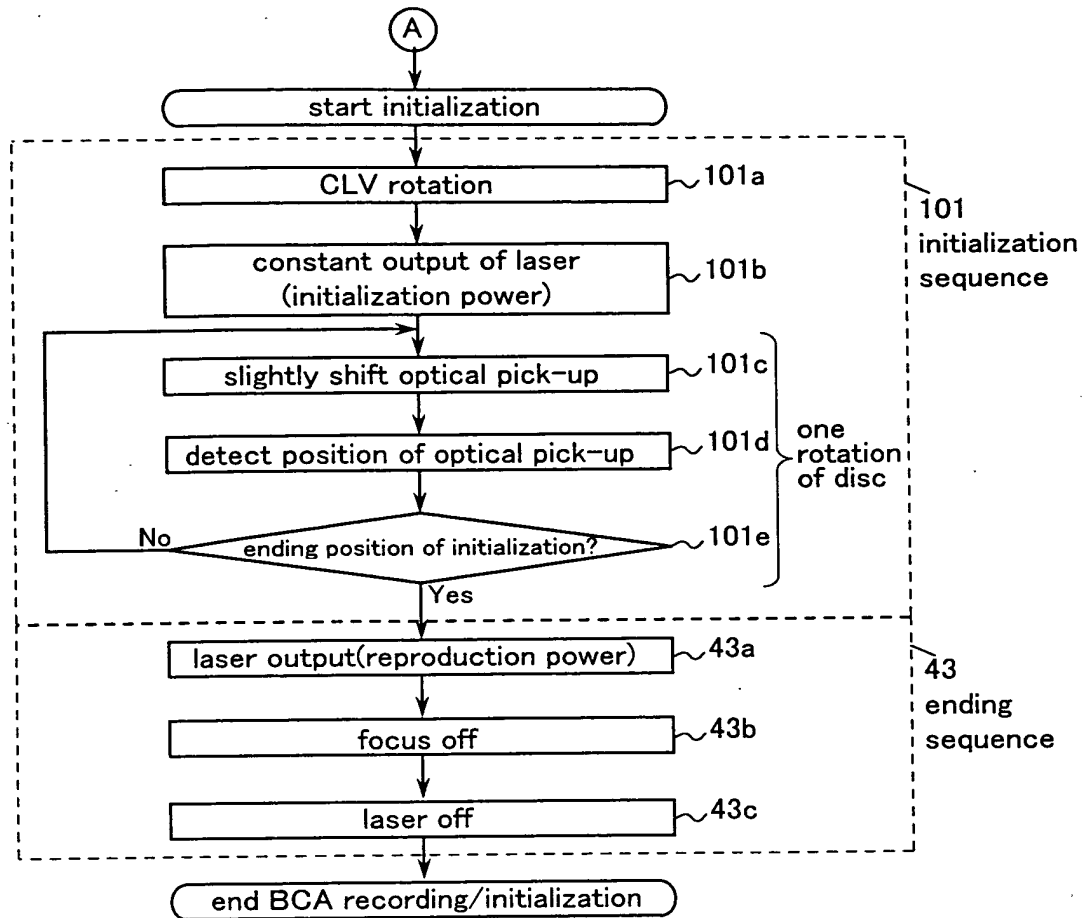
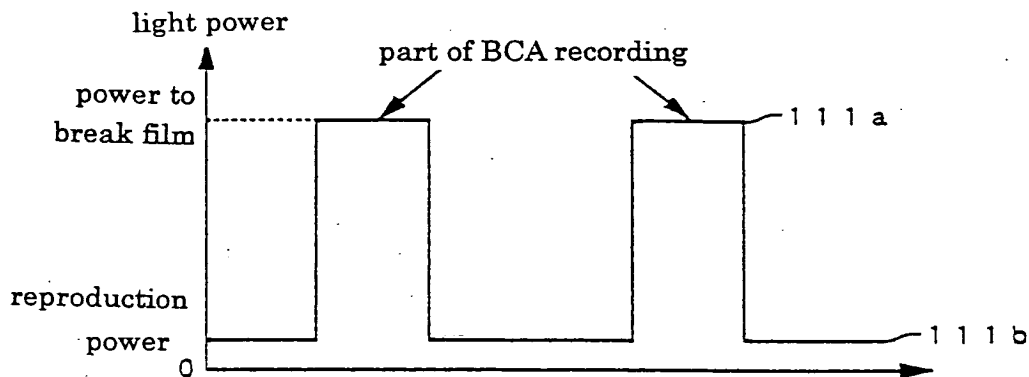


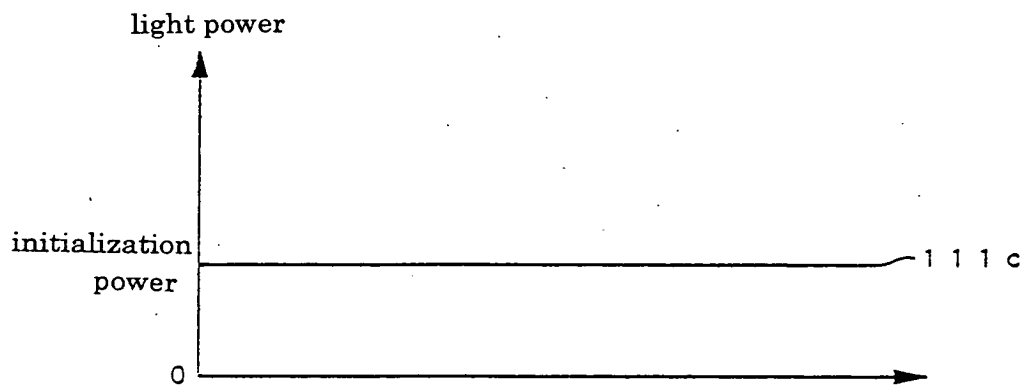
FIG. 10

FIG. 11

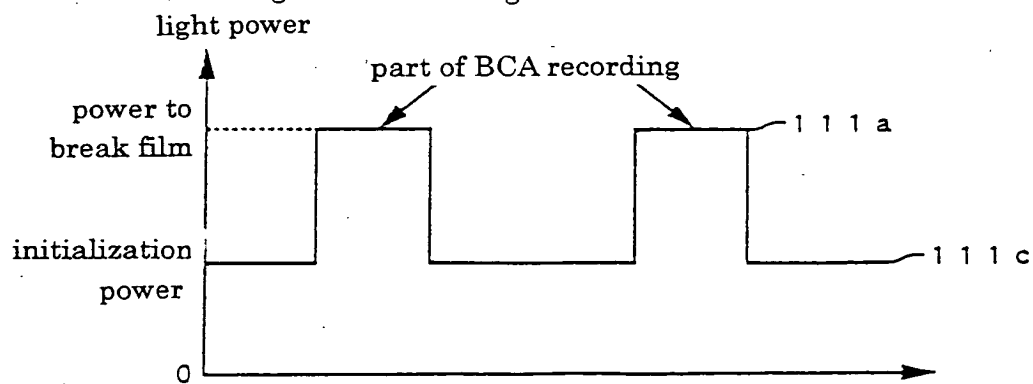
(1) during BCA recording 1



(2) during initialization



(3) during BCA recording 2



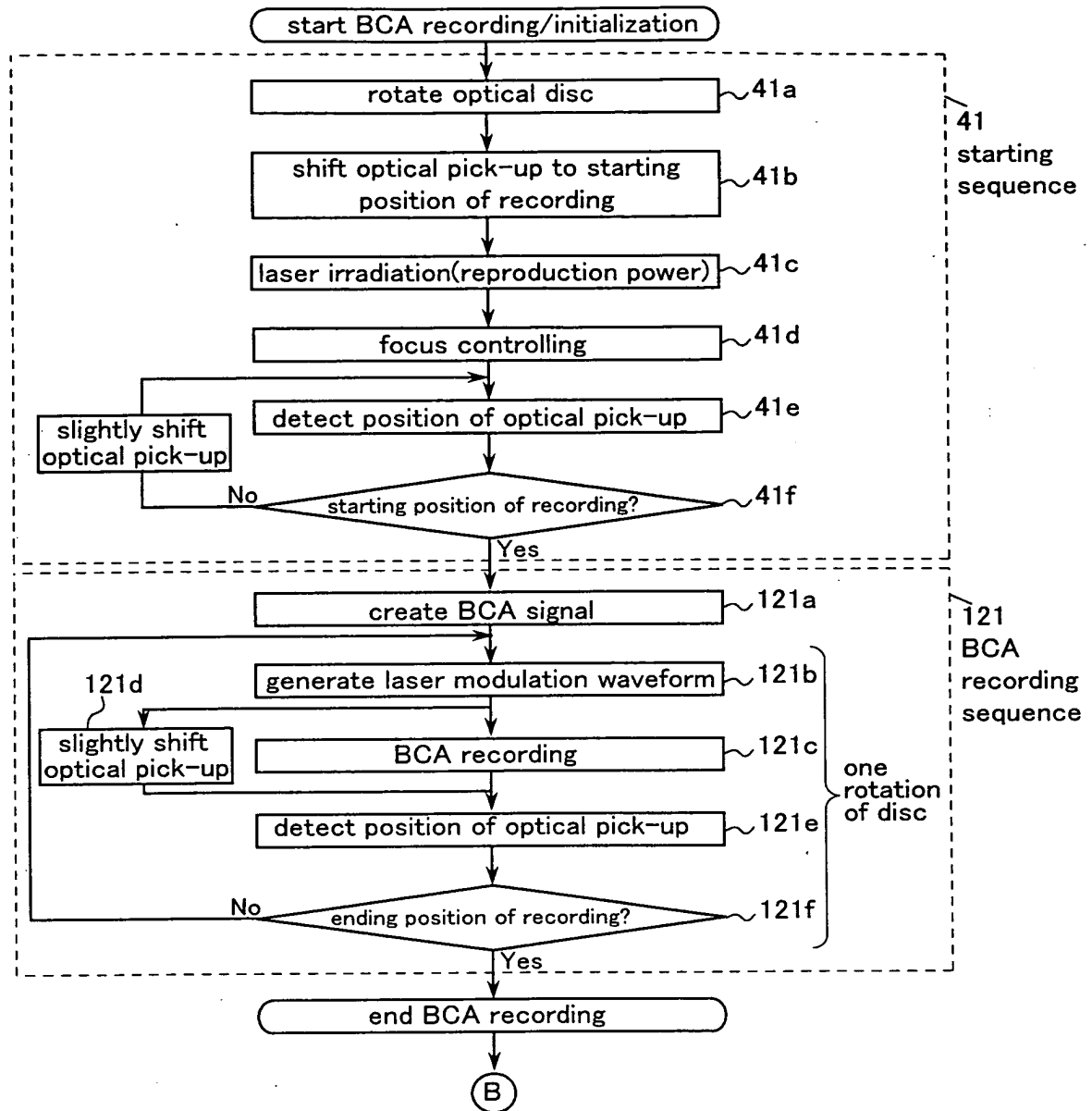


FIG. 12

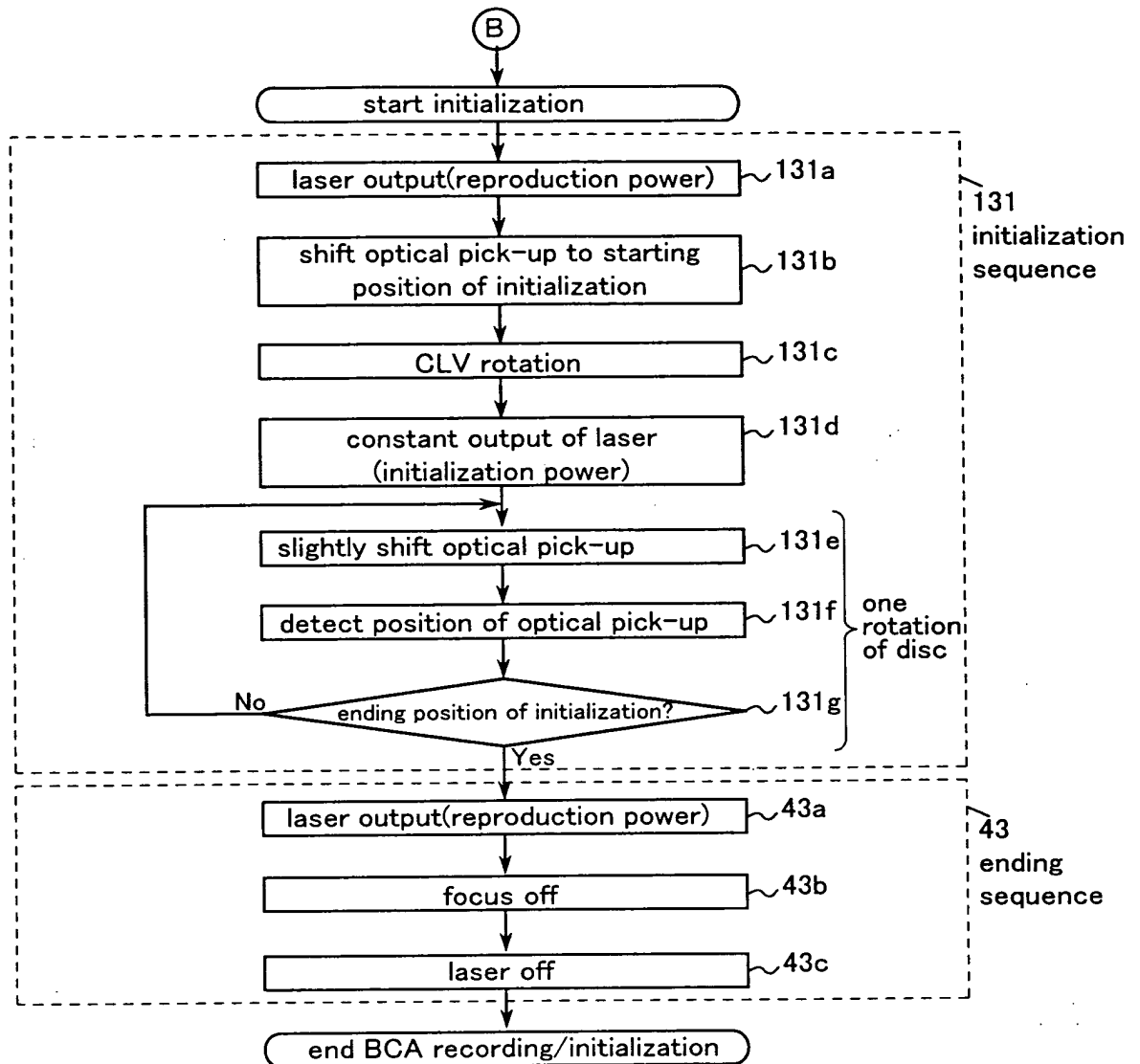


FIG. 13

FIG. 14

(1) BCA recording by laser

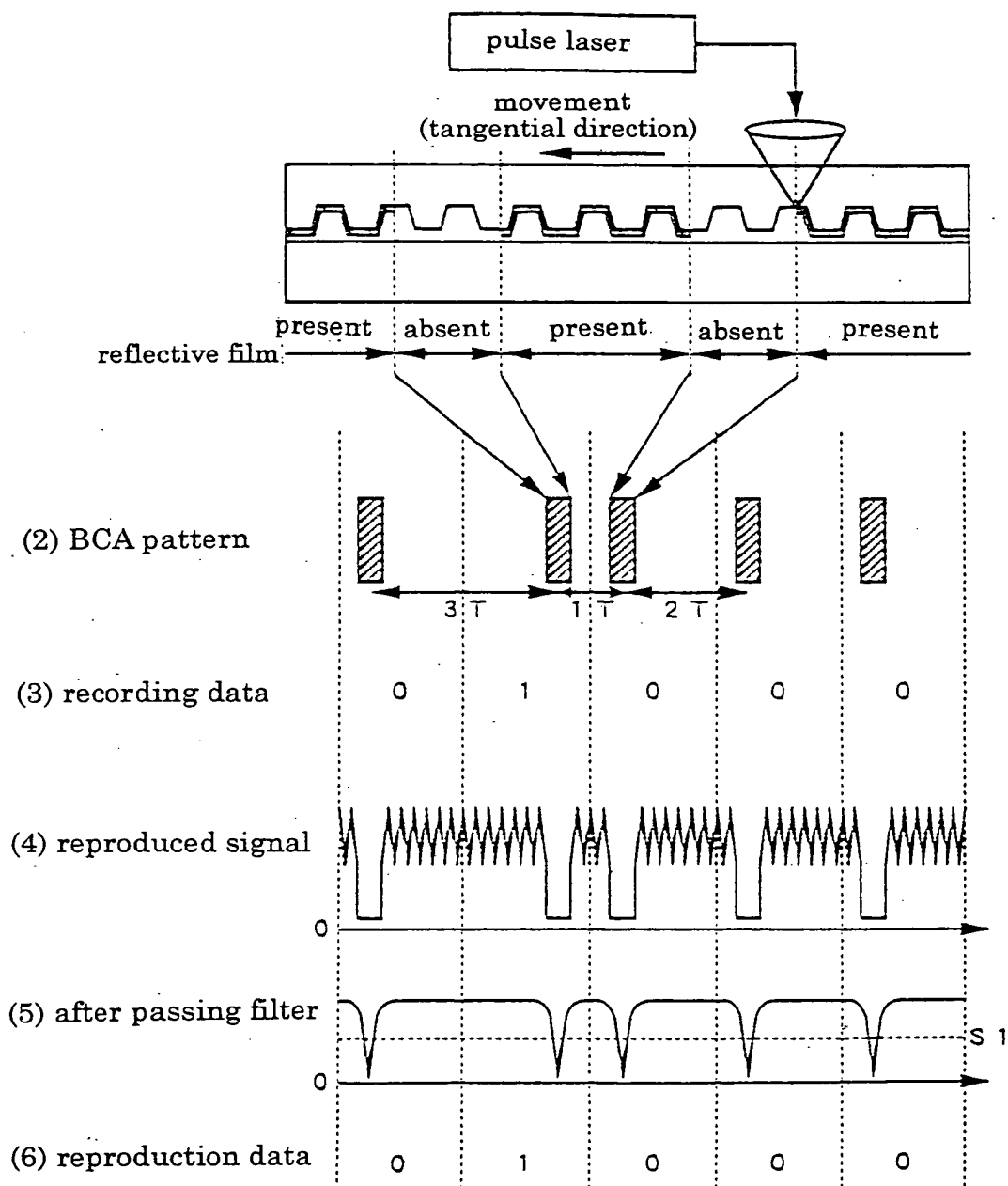


FIG. 15

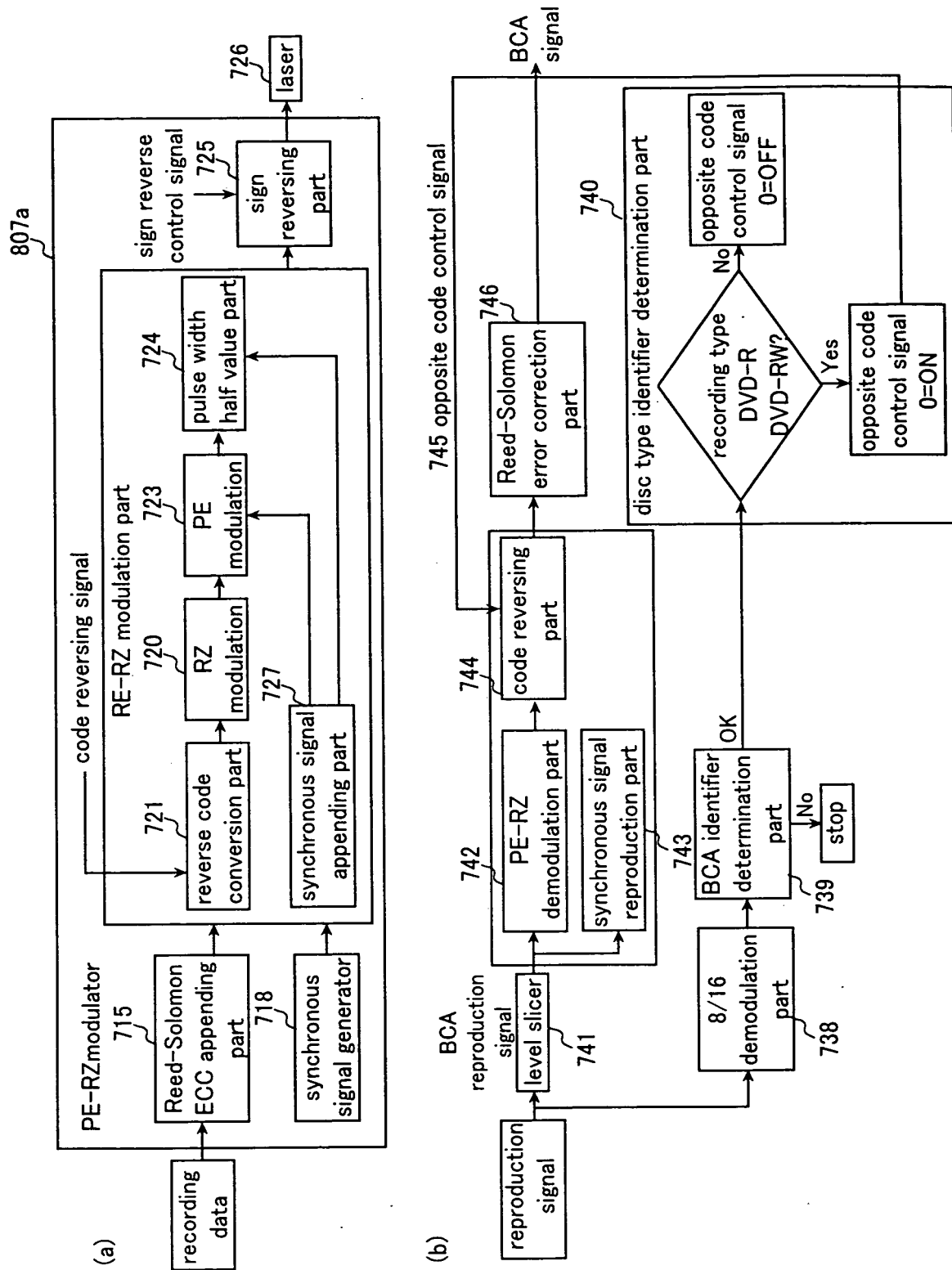


FIG. 15

(a) data configuration (n=12, 188 bytes)

(b) data configuration (n=1, 12 bytes)

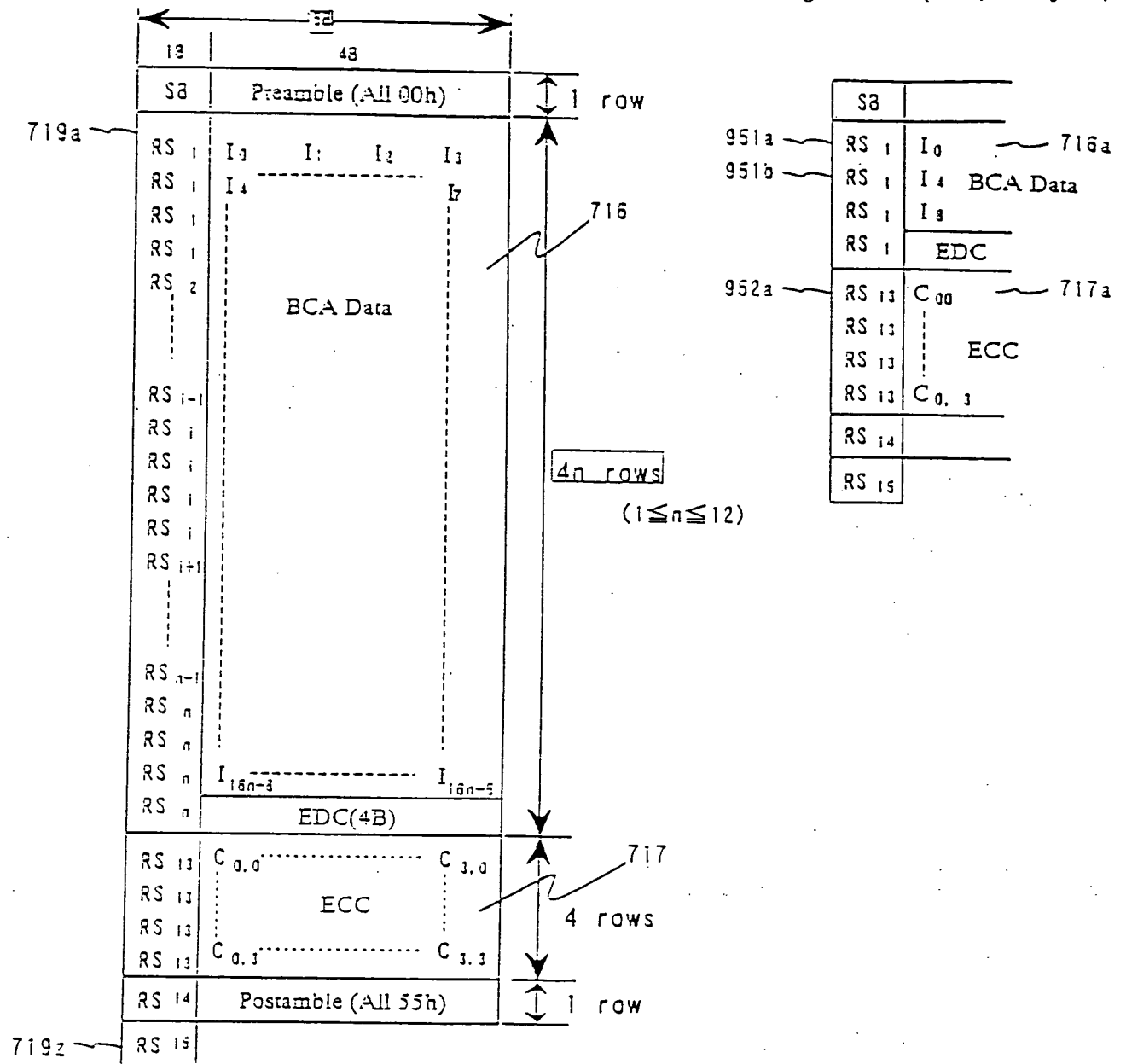
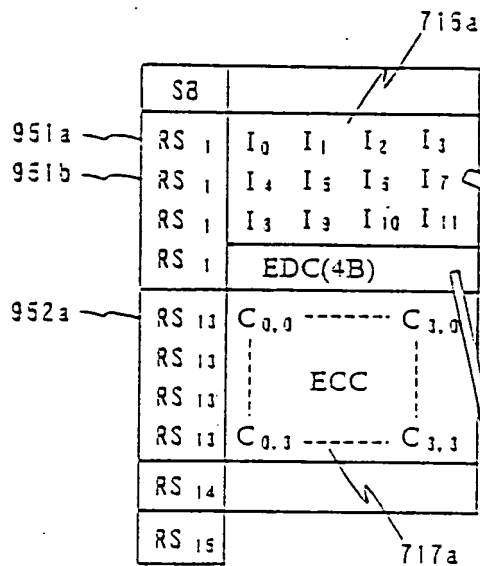




FIG. 17

(a) data configuration (n=1, 12 bytes)



(b) virtual data configuration for ECC calculation (n=1, 12 bytes)

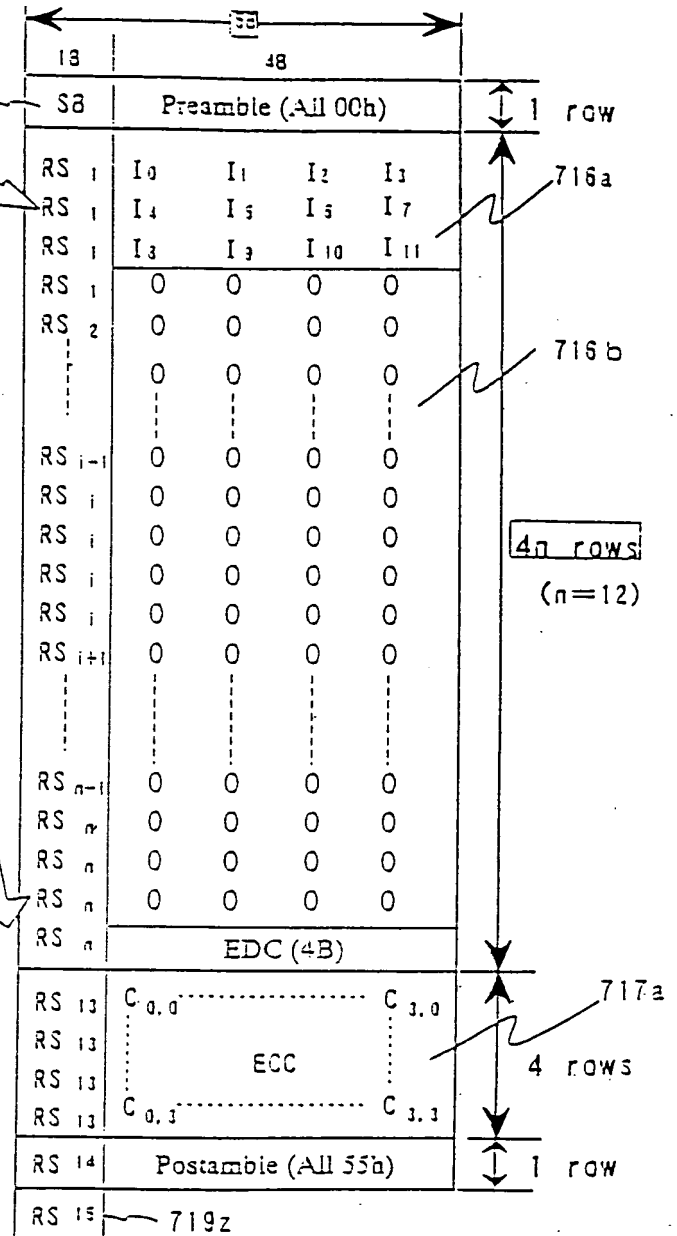


FIG. 18

(a) data of synchronous code      synchronous code

Sync Byte /Resync	Bit Pattern											
	Fixed Pattern								Sync Code			
	( Channel bit )								( Data bit )			
	C <sub>15</sub>	C <sub>14</sub>	C <sub>13</sub>	C <sub>12</sub>	C <sub>11</sub>	C <sub>10</sub>	C <sub>9</sub>	C <sub>8</sub>	b <sub>3</sub>	b <sub>2</sub>	b <sub>1</sub>	b <sub>0</sub>
791a S8	0	1	0	0	0	1	1	0	0	0	0	0
791b RS 1	0	1	0	0	0	1	1	0	0	0	0	1
RS 2	0	1	0	0	0	1	1	0	0	0	1	0
⋮												
RS i	0	1	0	0	0	1	1	0				
⋮												
791z RS <sub>15</sub>	0	1	0	0	0	1	1	0	1	1	1	1

(b) fixed synchronous pattern

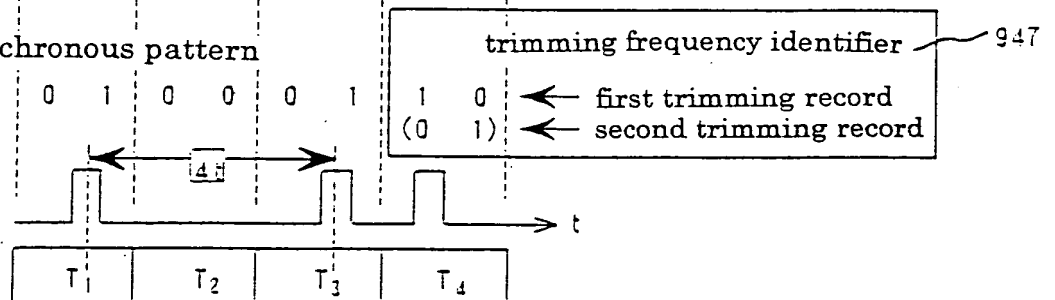


FIG. 19

modulation signal of ROM disc

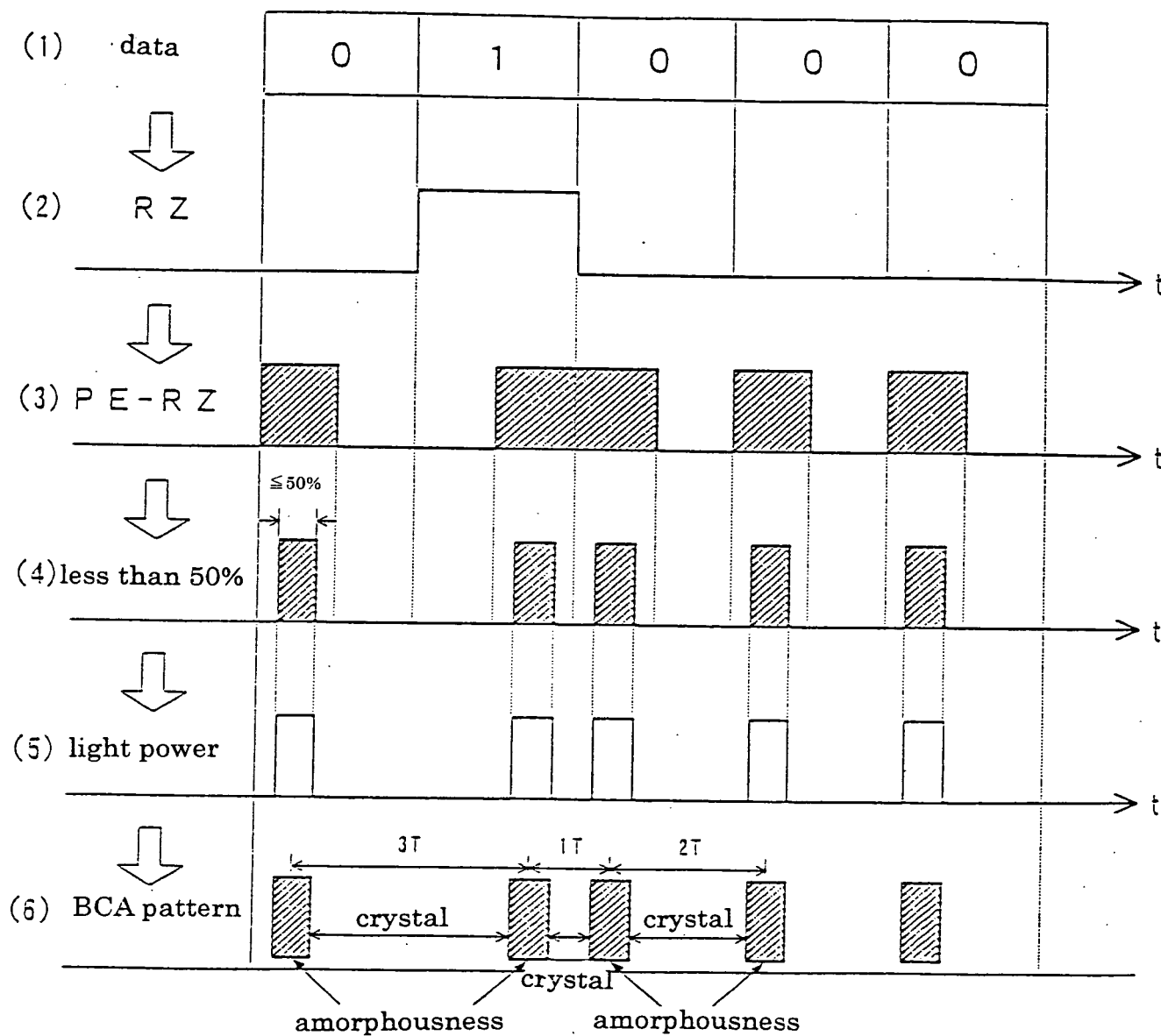


FIG. 20

modulation signal of RAM disc

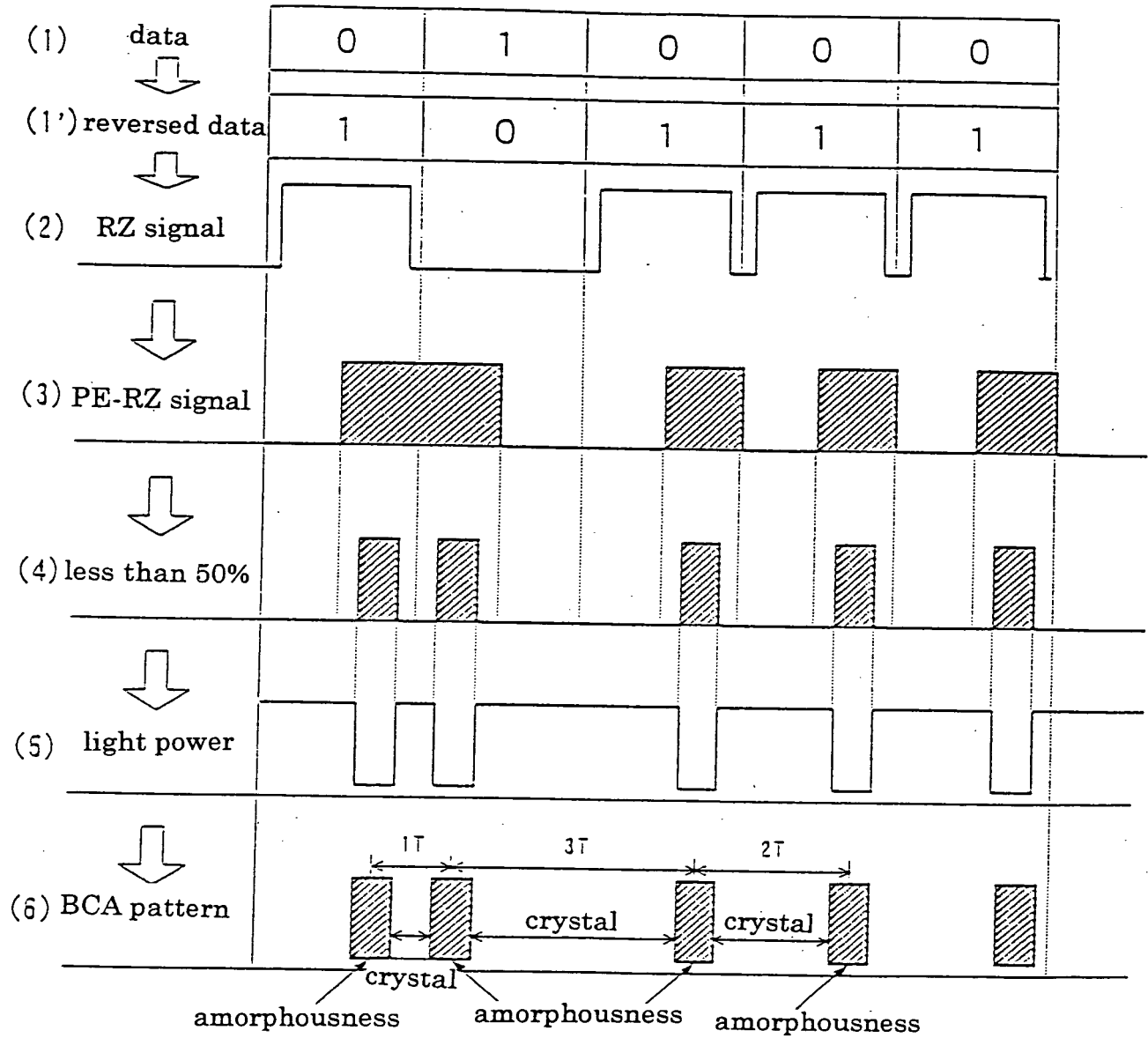


FIG. 21

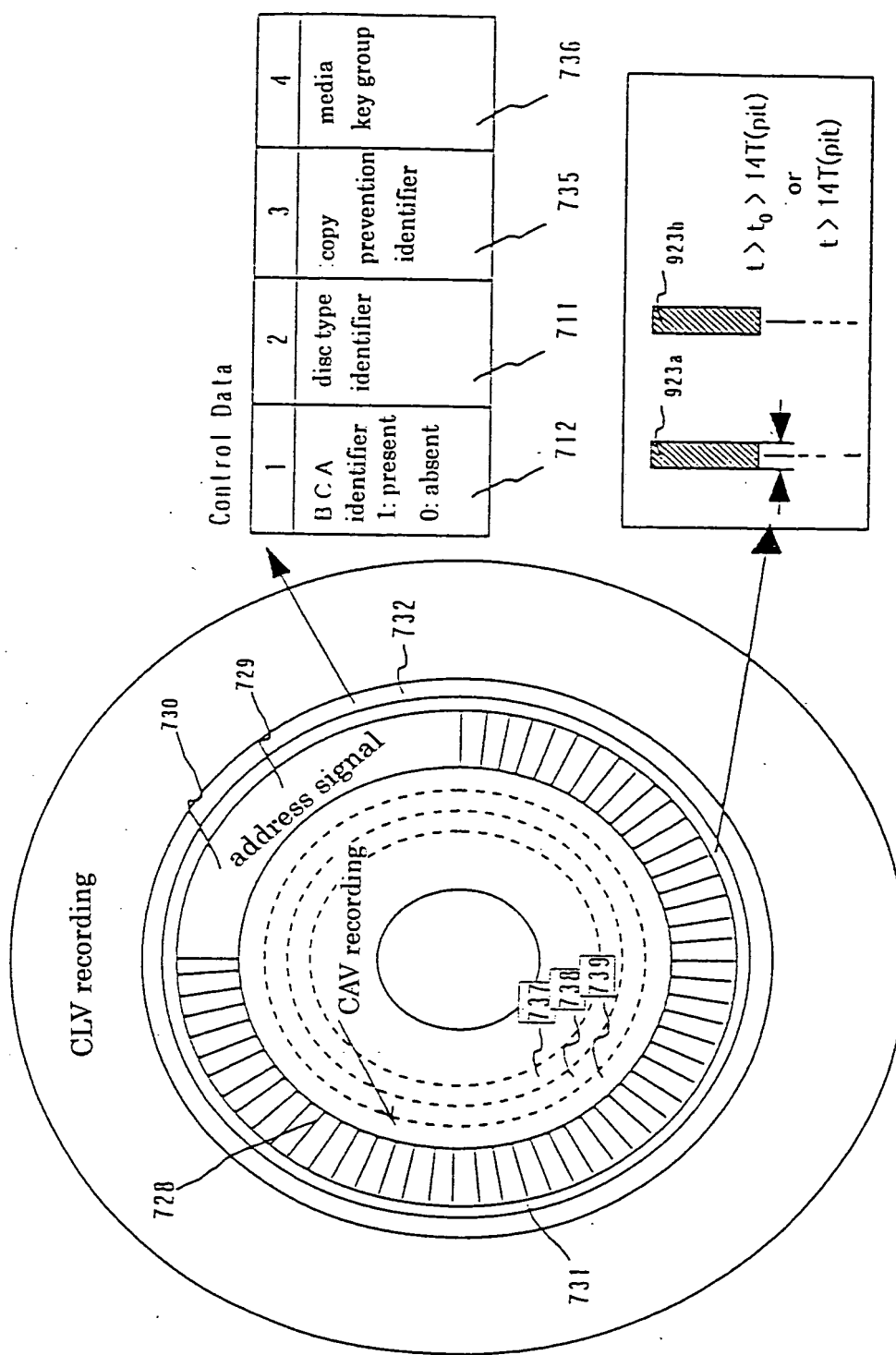


FIG. 22

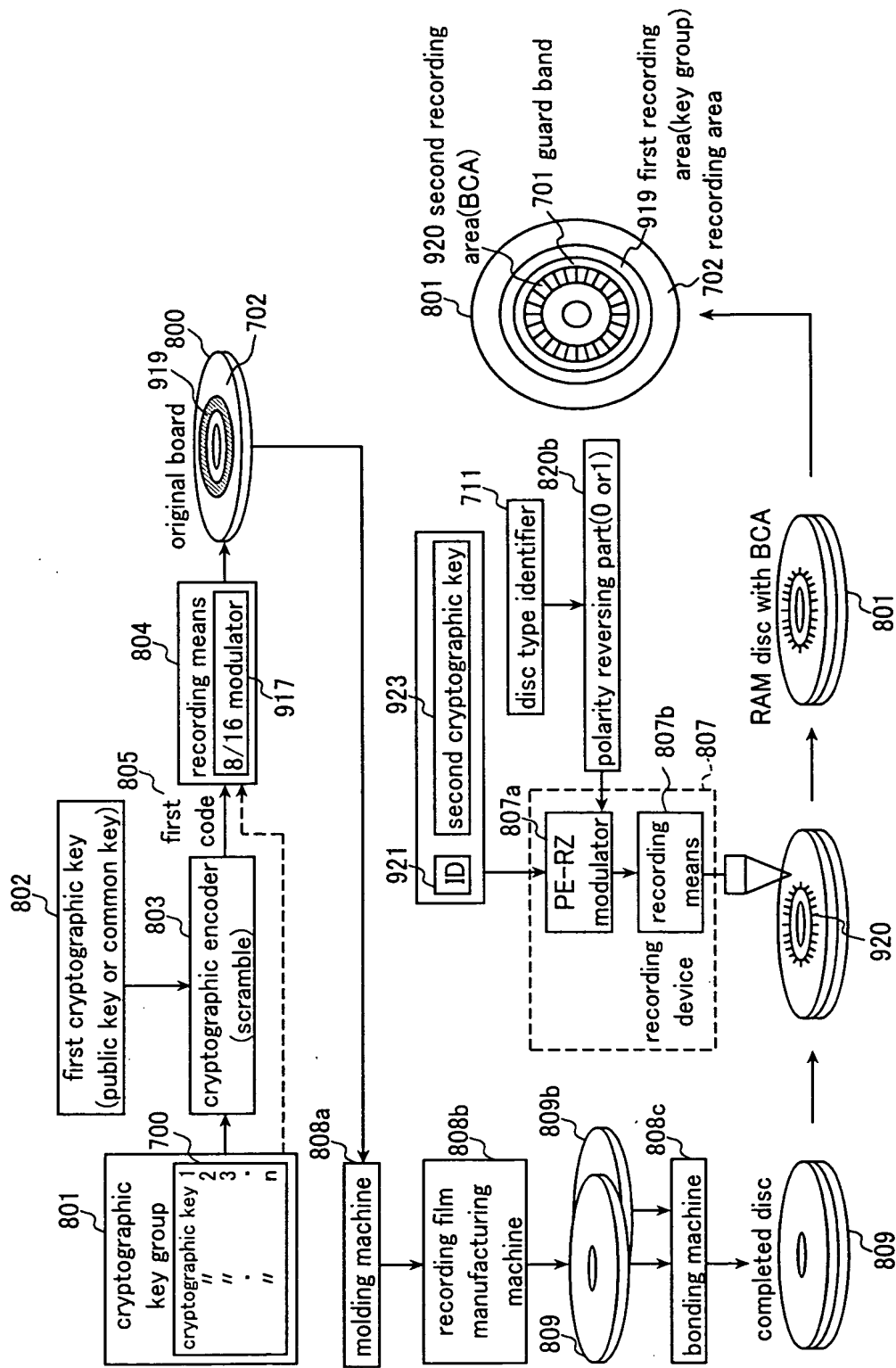


FIG. 22

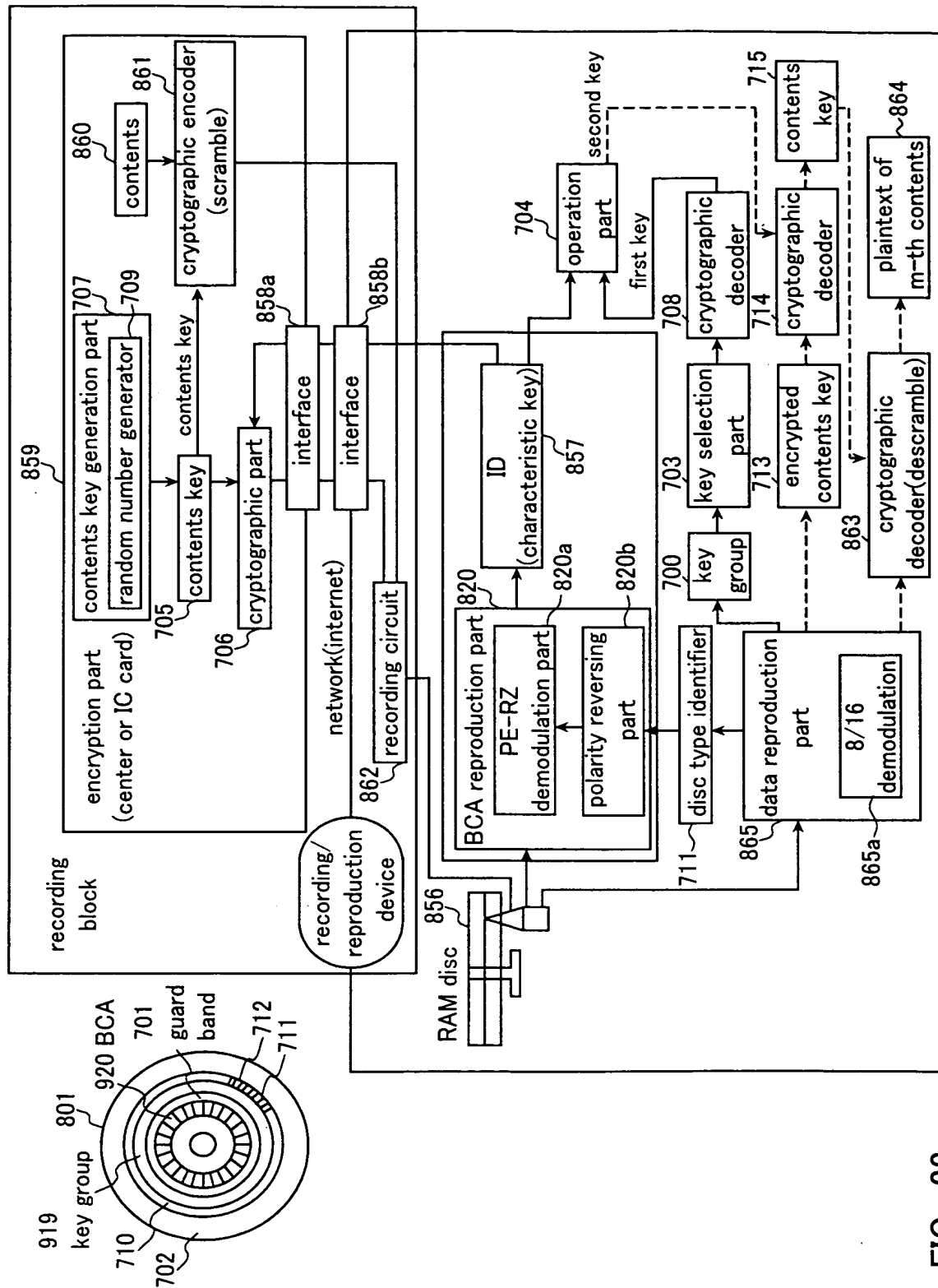


FIG. 23

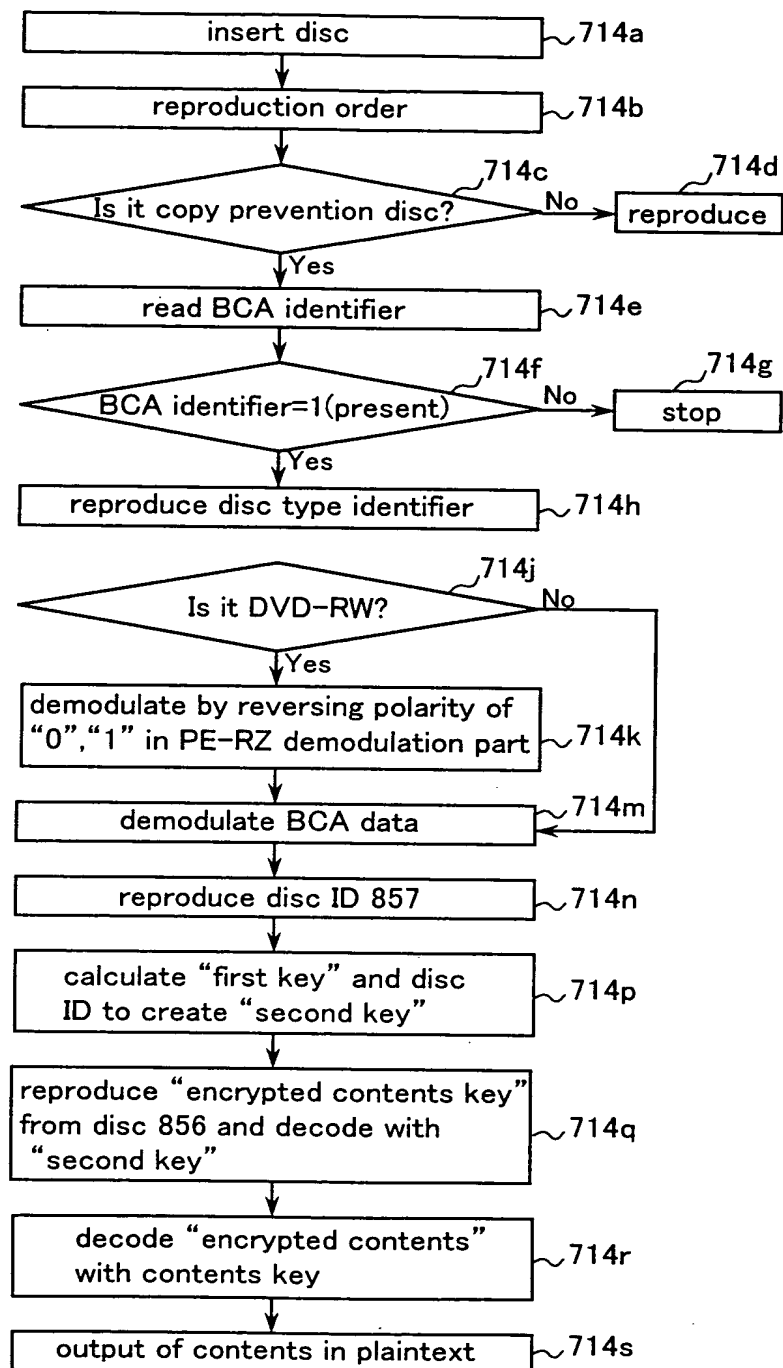


FIG. 24